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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/710,506	07/16/2004	Ta-Shuang Kuan	NAUP0588USA	4505
27765	7590 10/14/2005		EXAMINER	
NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION			CHIEN, LUCY P	
P.O. BOX 506 MERRIFIELD			ART UNIT	PAPER NUMBER
WEIGHT IEEE	, , , , , , , , , , , , , , , , , , , ,	71 22110	2871	-

DATE MAILED: 10/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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	·	Application No.	Applicant(s)	,	
Office Action Summary		10/710,506	KUAN ET AL.		
		Examiner	Art Unit		
		Lucy P. Chien	2871		
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with	the correspondence address		
WHI0 - Exte after - If N0 - Failt Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING Discussions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period or the toreply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing led patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICA 36(a). In no event, however, may a reprivate a reprivate and will expire SIX (6) MONTHS, cause the application to become ABA	ATION. ly be timely filed IS from the mailing date of this communication. NDONED (35 U.S.C. § 133).		
Status					
1)	Responsive to communication(s) filed on	·			
2a) <u></u> □	☐ This action is FINAL . 2b) ☐ This action is non-final.				
3)	Since this application is in condition for allowar	•	•		
	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D.	11, 453 O.G. 213.		
Disposit	ion of Claims				
5)□ 6)⊠ 7)□	Claim(s) 1-30 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) 1-30 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/o	wn from consideration.	·		
Applicat	ion Papers				
10)⊠	The specification is objected to by the Examine The drawing(s) filed on 16 July 2004 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	☑ accepted or b)☐ objecte drawing(s) be held in abeyance tion is required if the drawing(s)	e. See 37 CFR 1.85(a). is objected to. See 37 CFR 1.121(d).		
Priority (under 35 U.S.C. § 119				
а)	Acknowledgment is made of a claim for foreign All b) Some * c) None of: Certified copies of the priority document Certified copies of the priority document Copies of the certified copies of the priority document pplication from the International Bureau See the attached detailed Office action for a list	is have been received. Is have been received in Apprixity documents have been received in	olication No eceived in this National Stage		
Attachmer	• •				
2) Notice 3) Infor	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date		Mail Date mal Patent Application (PTO-152)		

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DETAILED ACTION

Claim Rejections - 35 USC § 102

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claim 1-7,18-23 are rejected under 35 U.S.C. 102(e) as being anticipated by Koike (US 20040150777).

Regarding Claim 1,

Koike discloses (Figure 13) a silicon substrate (1) having a plurality of pixels arranged in a pixel array thereon, each of the pixels comprising a plurality of subpixels, a transparent substrate (20) positioned above the glass substrate which is made of silicon (1) the transparent substrate (20) having a top surface and a bottom surface, a liquid crystal layer (19) positioned between the transparent substrate (20) and the silicon substrate (1), and a plurality of micro color filters (37R,37G,37B) positioned on the top surface of the transparent substrate (20), each of the micro color filters being positioned corresponding one of the subpixels (shown in Figure 11 and Figure 12 which are the same embodiment as Figure 13).

Regarding Claim 2,19,

Koike discloses (Figure 13) a transparent conductive layer (25) positioned on the bottom surface of the transparent substrate (20).

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Regarding Claim 3,20,

Koike discloses (Figure 13) the transparent conductive layer is an indium tin oxide layer (Page 4, [0083]).

Regarding Claim 4,21,

Koike discloses (Figure 13) a top alignment layer (26) positioned between the transparent substrate (20) and the liquid crystal layer (19).

Regarding claim 5,22,

Koike discloses (Figure 13) a bottom alignment layer (14) positioned between the silicon substrate (1) and the liquid crystal layer (19).

Regarding Claim 6,23,

Koike discloses (Figure 13) the transparent substrate (20) is a glass substrate. (Page 4, [0078]).

Regarding Claim 7,

Koike discloses (Figure 13) the micro color filters (37R,37G,37B) comprise a plurality of red micro color filters (37R), and a plurality of blue micro color filters (37B), and a plurality of green micro color filters (37G), each of the red micro color filters, the blue micro color filters, and the green micro color filters being positioned correspoiding one of the subpixels of each of the pixels (Figure 12 shows subpixels (RGB) in a pixel).

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Regarding Claim 18,

Koike discloses (Figure 13) a silicon substrate (1) having a pixel region thereon, a transparent substrate (20) positioned above the silicon substrate (1), the transparent substrate (20) having a top surface and a bottom surface, a liquid crystal layer (19) positioned between the silicon substrate (1) and the transparent substrate (20), and at least a color filter (37R,37G,37B) positioned on the top surface of the transparent substrate (20) and corresponding to the pixel region.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 8-17,24-30 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koike (US 20040150777) in view of Nakano et al (US 4350729).

Regarding Claim 8,24,

Koike does not discloses the micro color filters comprise photosensitive material.

Nakano et al discloses the micro color filters comprise photosensitive material. (Column 5, Rows 5-10).

It would have been obvious to one of ordinary skill in the art at the time of the invention to modify Koike's display to include Nakano et al's color filter's photosensitive

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material, which has a chemical reaction when light strikes it thus creating a desired

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color filter with excellent light stability. (Column 3, Rows 30-35).

Regarding Claim 9,25,

In addition to Koike and Nakano et al as disclosed above, Nakano further discloses the

color filters dyes. (Column 3, Rows 5-25).

Regarding Claim 10,26,

In addition to Koike and Nakano et al as disclosed above, Nakano further discloses the

photosensitive material are photoresist materials or photoresist resins. (Column3, Rows

34-40).

Regarding claim 11,27,

In addition to Koike and Nakano et al as disclosed above, Nakano further discloses

each of the micro color filters comprises a plurality of optical thin films. (Column3, Rows

18-25).

Regarding Claim 12,28,

In addition to Koike and Nakano et al as disclosed above, Koike further discloses the

optical thin films are dichroic films. (Page 4, [0084]).

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Regarding Claim 13,

In addition to Koike and Nakano et al as disclosed above, Nakano et al discloses each

micro color filters is an optical thin-film stack. (Column 3, Rows 18-25).

Regarding Claim 14,29,

In addition to Koike and Nakano et al as disclosed above, Nakano et al discloses the

optical thin-film stacks are low index optical thin-film stacks comprising silicon oxide thin

films. (Column 3, Rows 18-25) and (Column 4, Table)

Regarding Claim 15,30,

In addition to Koike and Nakano et al as disclosed above, Nakano et al discloses the

optical thin-film stacks are high index optical thin-film stacks comprising titanium oxide

thin films or tantalum oxide thin films. (Column3, Rows 18-25) and (Column 4, Table)

Regarding Claim 16,

In addition to Koike and Nakano et al as disclosed above, Nakano et al discloses when

light enters the display panel, lights of a first specific spectrum, a second specific

spectrum, and a third specific spectrum are capable of passing through the

corresponding micro color filters. (Column3, Rows 18-25).

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Regarding Claim 17,

In addition to Koike and Nakano et al as disclosed above, Nakano et al discloses lights of the first specific spectrum, the second specific spectrum and the third specific

spectrum are red, blue, and green lights respectively. (Column3, Rows 18-25).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lucy P. Chien whose telephone number is 571-272-8579. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571)272-2293. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Lucy Chien Examiner Art Unit 2871 LC

HALL SCHECHTER ANDREW SCHECHTER PRIMARY EXAMINER